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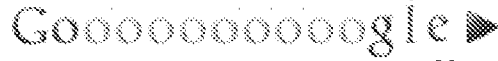
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... it into a low-level **intermediate** representation (IR **...** indirect branch target addresses (application **binary** addresses **...** entry is found for the **dynamic** indirect branch **...**

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